

Performance Metrics for Small-Signal Stability Assessment of DC-Distributed Power-System-Architecture Comparisons

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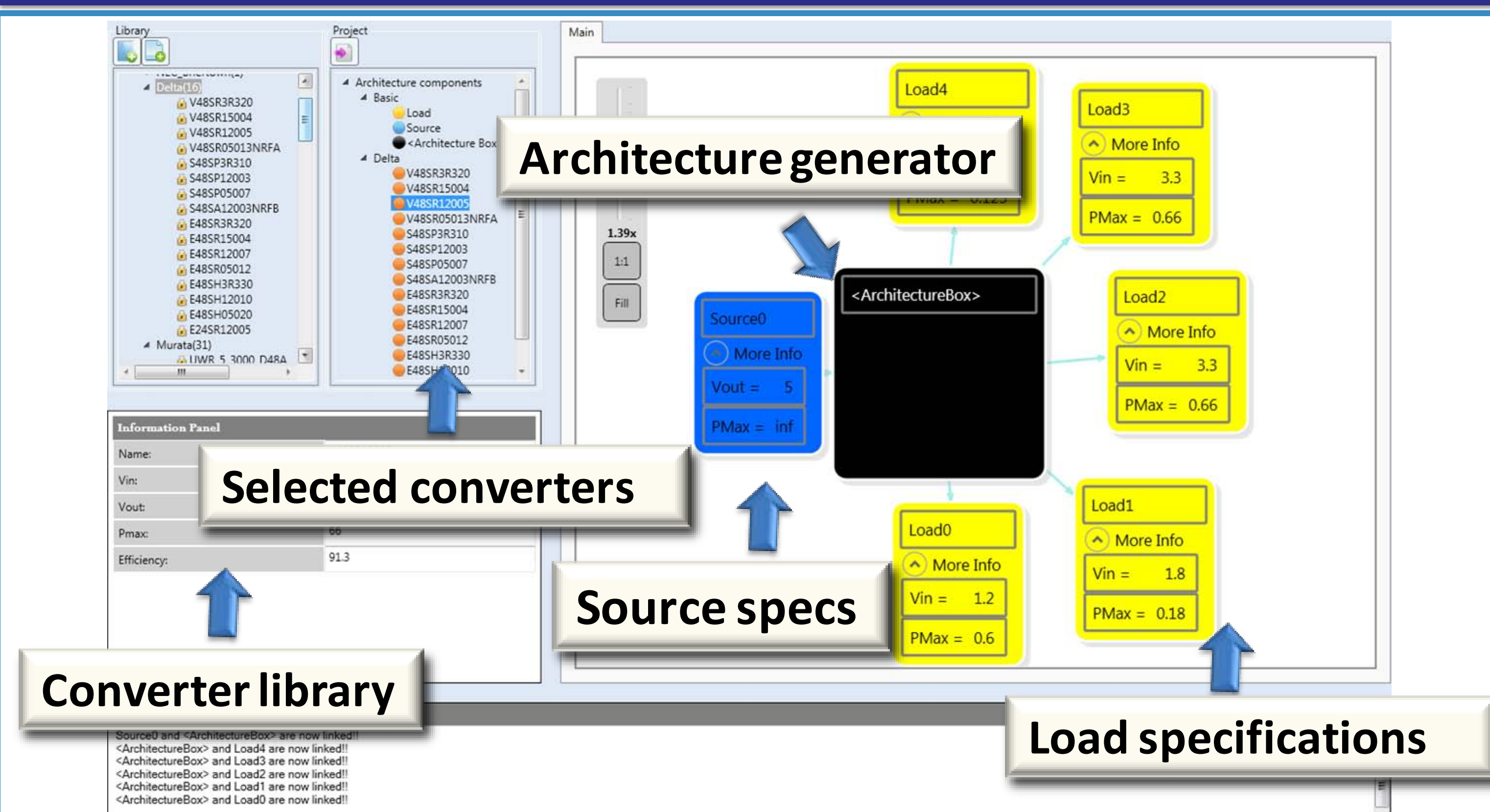
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1. ARCHITECTURE OPTIMIZATION TOOL

User interface



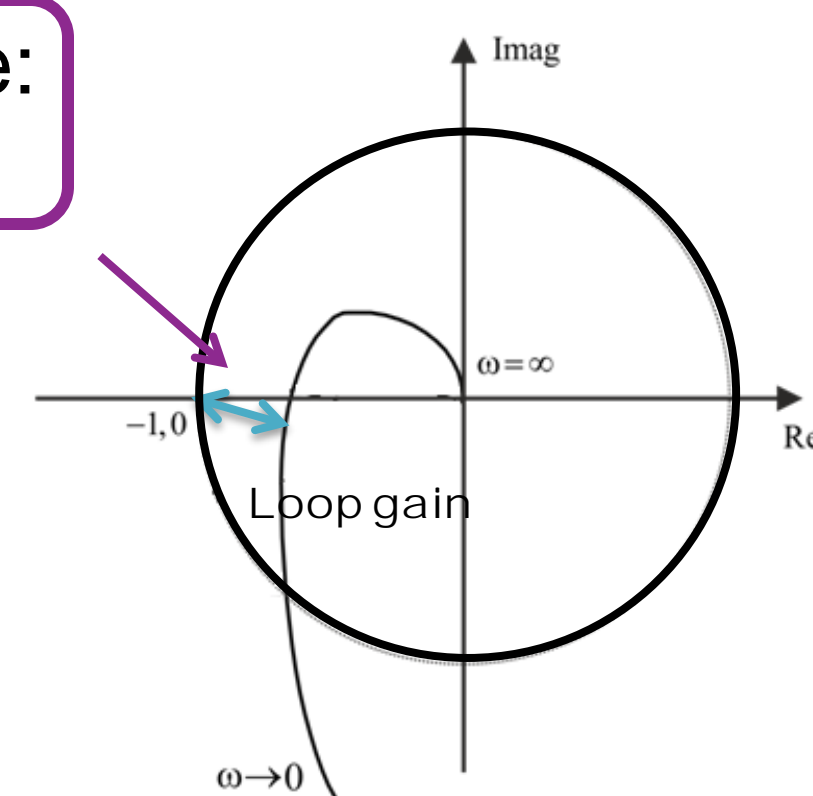
2. STABILITY MARGINS

How close stable system is to instability

Maximum peak criteria MPC

- Prediction of robust stability
- Based on min. distance between -1 and L

Min. distance:
 $1/S_{max}$



Sensitivity function

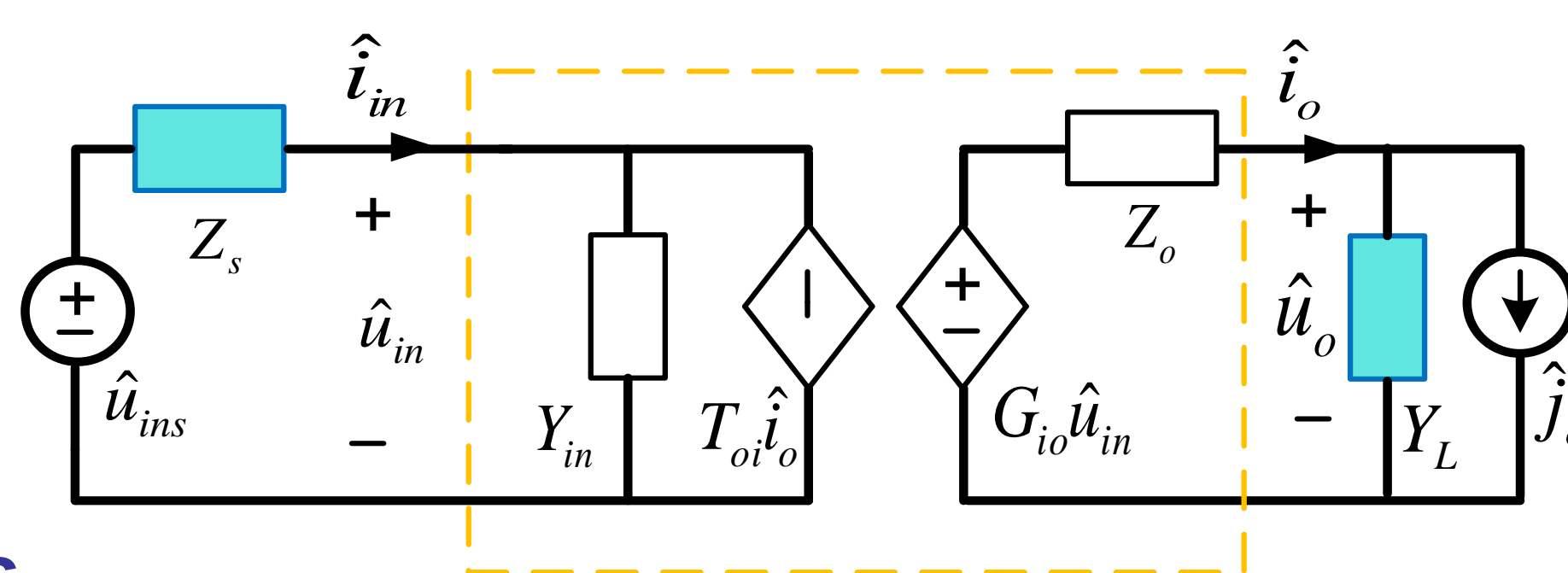
- Both margins:

$$S = \frac{1}{1+L}$$

$$GM \geq \frac{1}{1-1/|S_{max}|} \quad PM \geq 2 \arcsin\left(\frac{1}{2|S_{max}|}\right) \Rightarrow \text{Typically } S_{max} < 2$$

Converter model

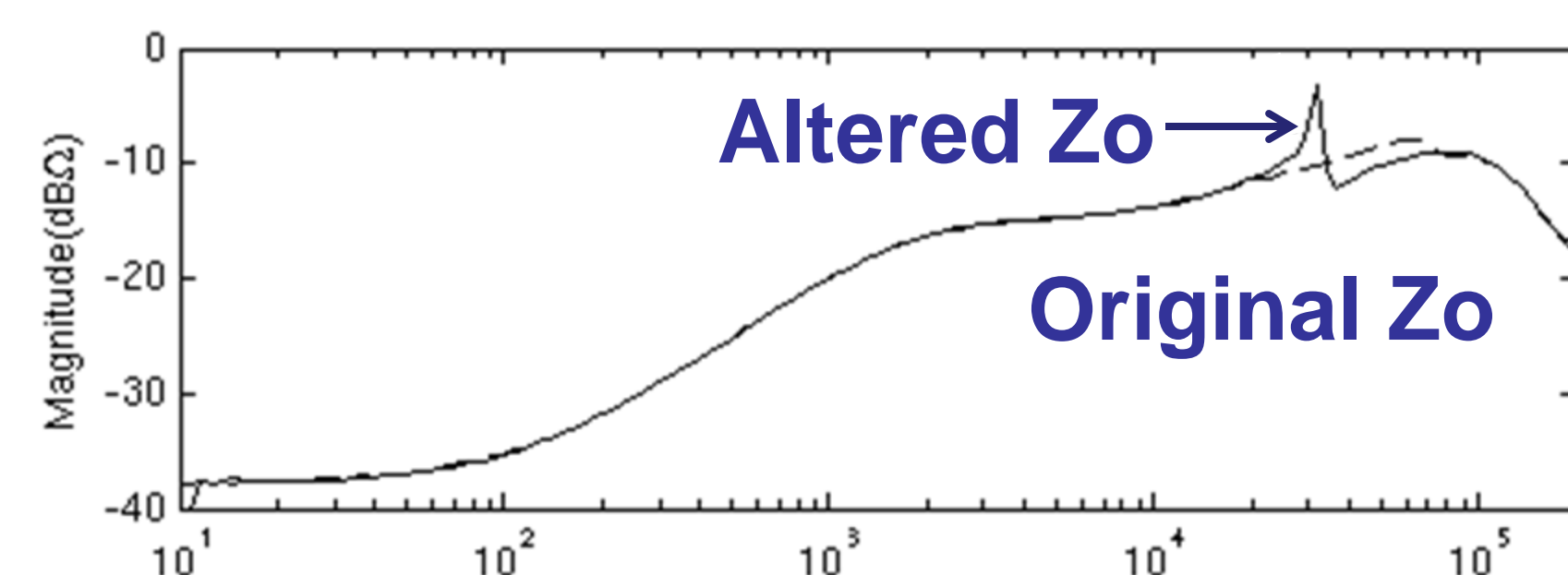
$$\begin{cases} \hat{i}_{in} = Y_{in} \hat{u}_{in} + T_{oi} \hat{i}_o \\ \hat{u}_o = G_{io} \hat{u}_{in} - Z_o \hat{i}_o \end{cases}$$



Source-affected dynamics

$$\begin{cases} \hat{i}_{in} = S \cdot Y_{in} \cdot \hat{u}_{ins} + S \cdot T_{oi} \cdot \hat{i}_o \\ \hat{u}_o = S \cdot G_{io} \cdot \hat{u}_{ins} - S \cdot (1 + Z_s Y_{in-sco}) Z_o \cdot \hat{i}_o \end{cases} \quad S = \frac{1}{1+ML}$$

- Altered converter dynamics due to large peak in the sensitivity function



3. STABILITY INDEX

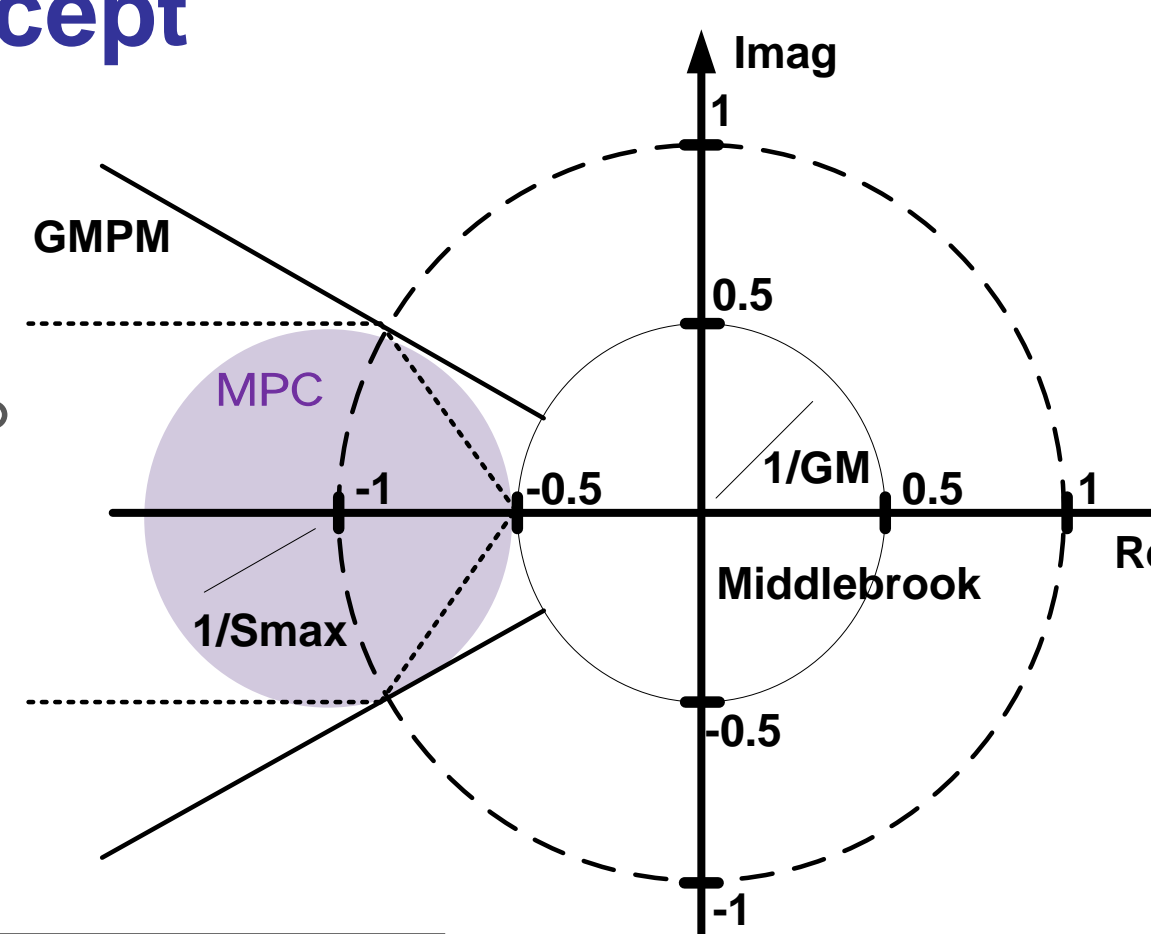
Overall system stability

Application of MPC concept

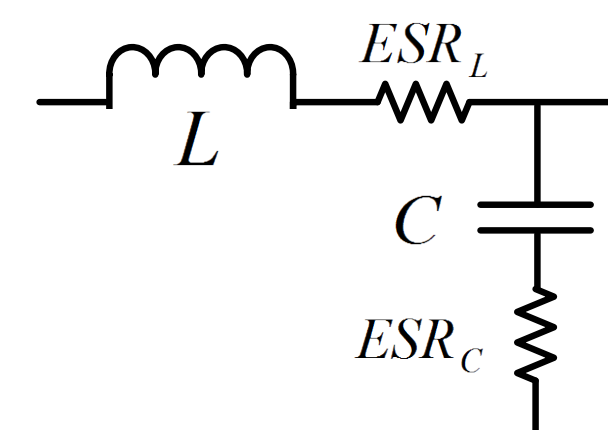
- $S_{max} = 2$ provides margins of
 $GM \geq 6\text{dB}$ and $PM \geq 29^\circ$
- Combined S_{max} of each interface

$$G.M. = \sqrt[n]{S_{max1} S_{max2} \cdots S_{maxn}}$$

- Best system in terms of robustness minimizes stability index
- Detection of the weakest interface H_∞



Input filter



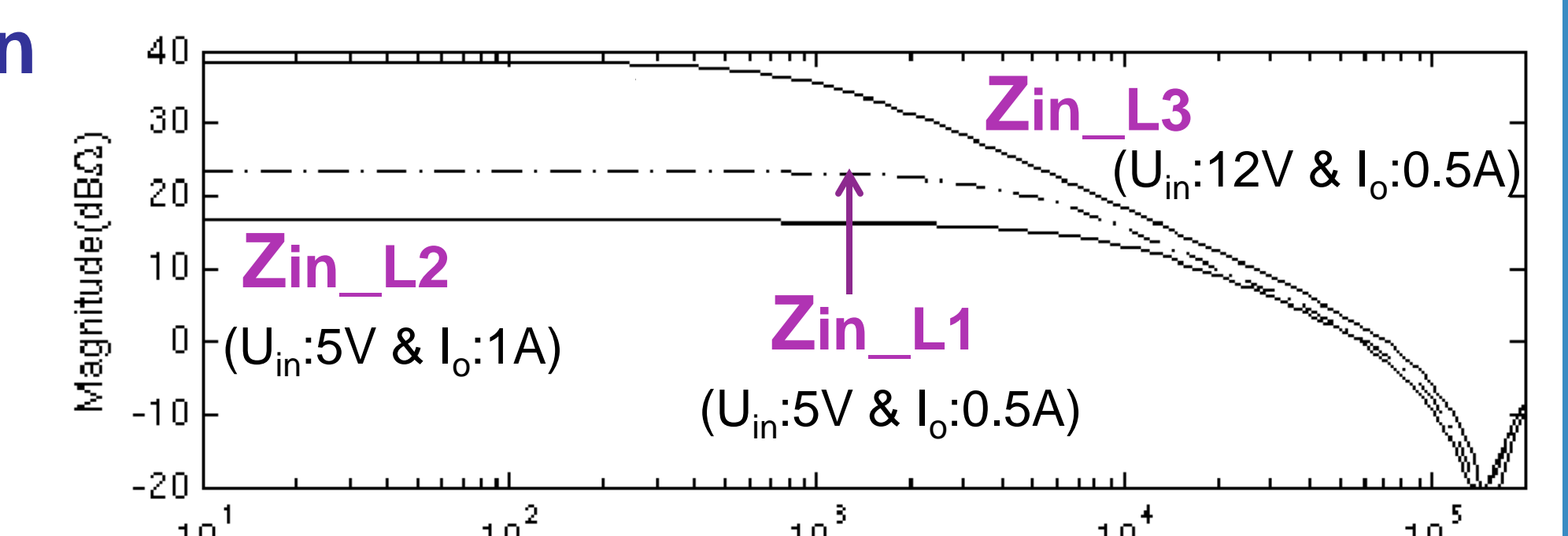
- F1** (L: 200 μ H, ESR_L : 100m Ω , C: 260 μ F, ESR_C : 100m Ω)
- F2** (L: 260 μ H, ESR_L : 160m Ω , C: 260 μ F, ESR_C : 100m Ω)
- F3** (L: 120 μ H, ESR_L : 160m Ω , C: 300 μ F, ESR_C : 100m Ω)

Utilized commercial converters

- Texas Instruments **M2** (U_{in} : 9-38V U_o : 5V P_{max} : 7.5W)
- Traco Power **M3** (U_{in} : 4.8-36V U_o : 3.3V P_{max} : 3.3W)
- National Semic. **M4** (U_{in} : 3.3-5.5V U_o : 3.3V P_{max} : 9.9W)

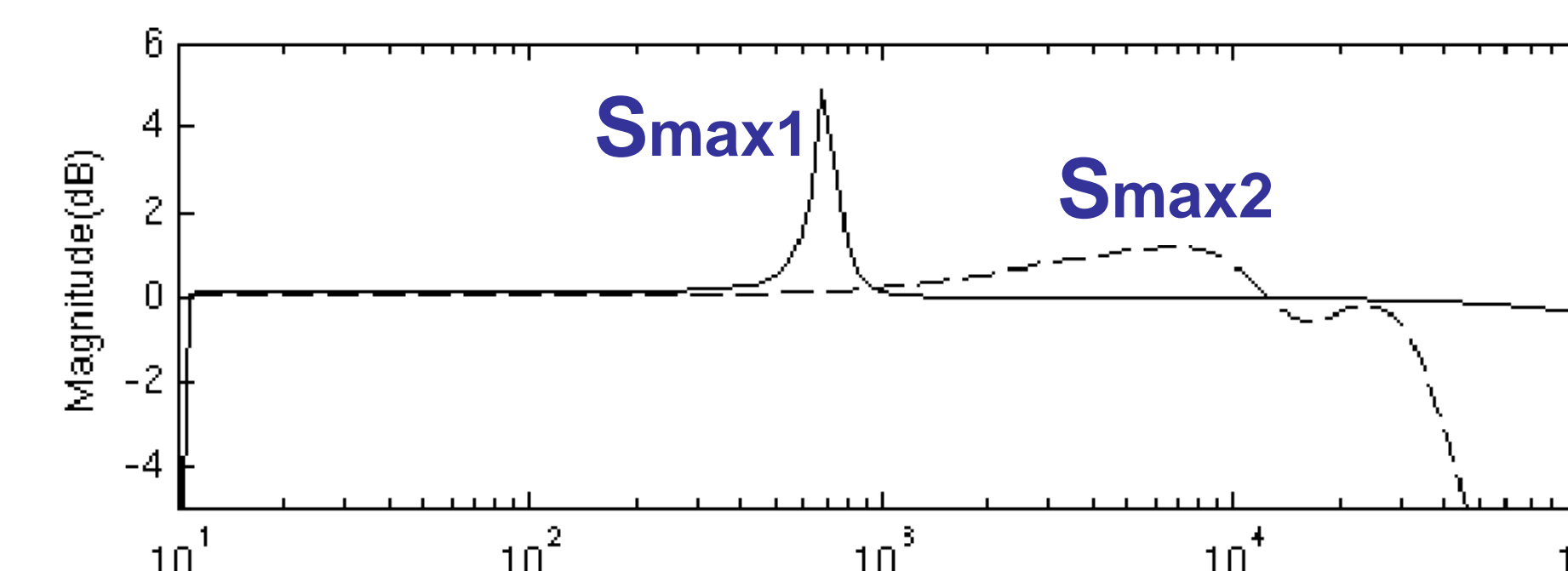
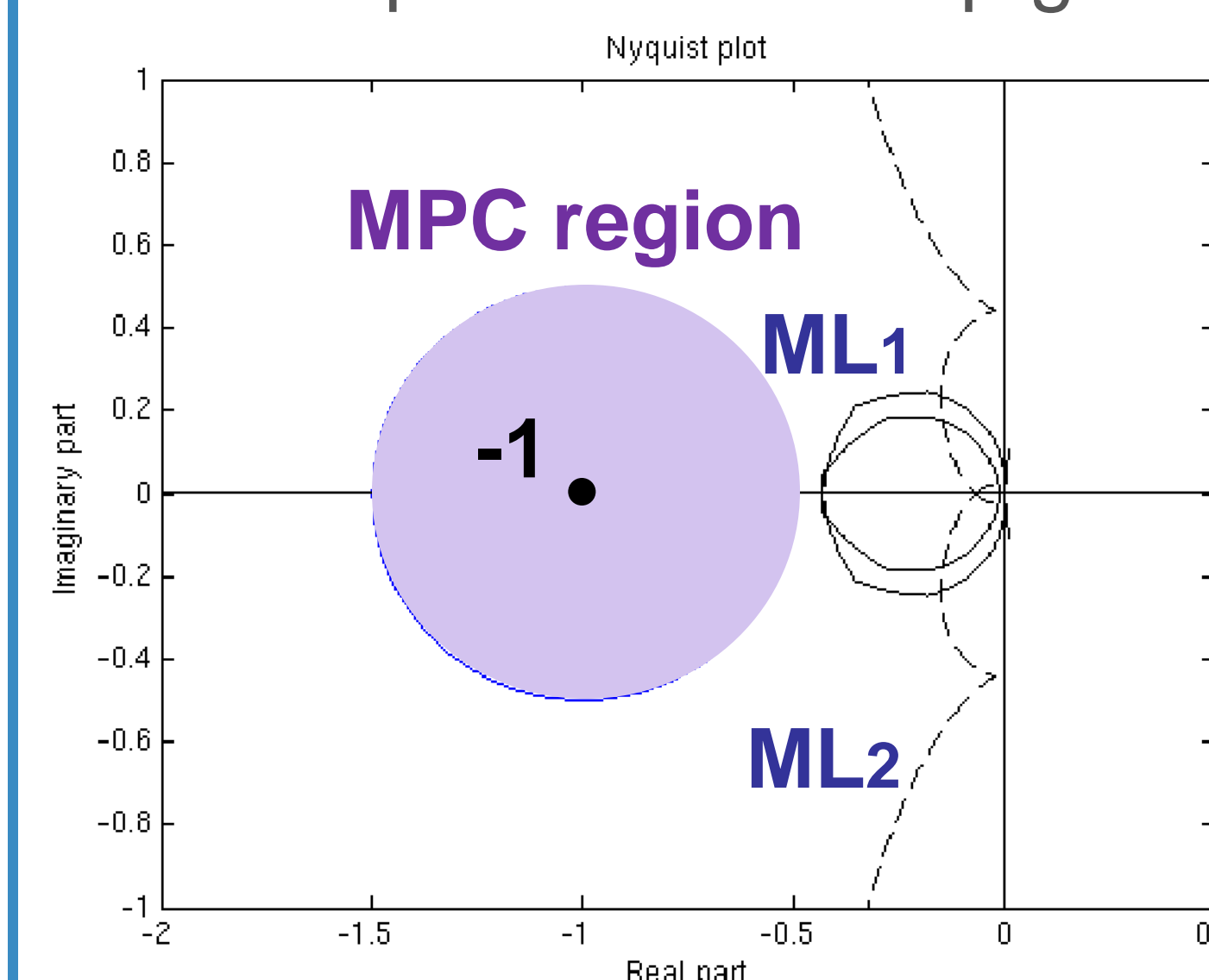
Converter characterization

- Measured Z_{in} of M3 for architecture **A3**



Stability margins for A1 architecture

- Computed minor-loop gains
- Computed S_{max} values for interfaces 1 and 2



Obtained stability indexes

Interface	A1	A2	A3
1. S_{max1}	1.75	1.66	1.2
2. S_{max2}	1.14	1.16	1.11
3. S_{max3}	1.16	-	1.16
G.M.	1.32	1.39	1.16
H_∞	1.75	1.66	1.2

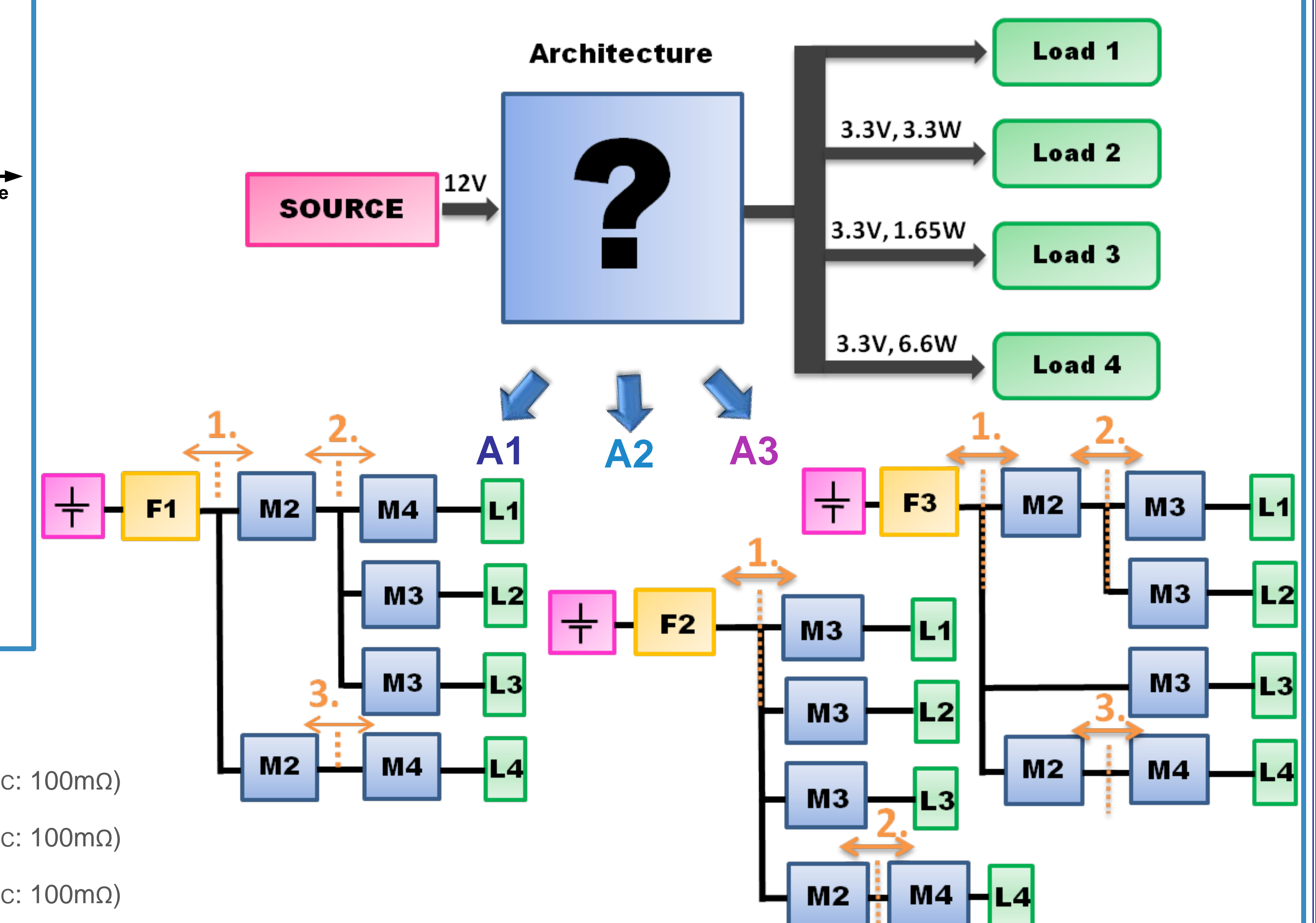
Limitations of the proposed method:

- Not sufficient condition, but **necessary** \Rightarrow Time-domain simulations for large signal stability
- Linear analysis \Rightarrow Operation-point dependent

4. ARCHITECTURE COMPARISON

Experimental validation

Static system specifications



Project sponsored by

